

CentralTM Semiconductor Corp.

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Manufacturer of World Class Discrete Semiconductors
www.centrasemi.com

C106A1 THRU C106M1

**SILICON CONTROLLED RECTIFIER
(100V to 600V)**

JEDEC TO-202 CASE

DESCRIPTION

The Central Semiconductor C106A1 Series are PNP Silicon Controlled Rectifiers designed for applications such as temperature, light, speed control, process and remote control, and warning systems where reliability of operation is important.

MAXIMUM RATINGS (T_C=25°C unless otherwise noted)

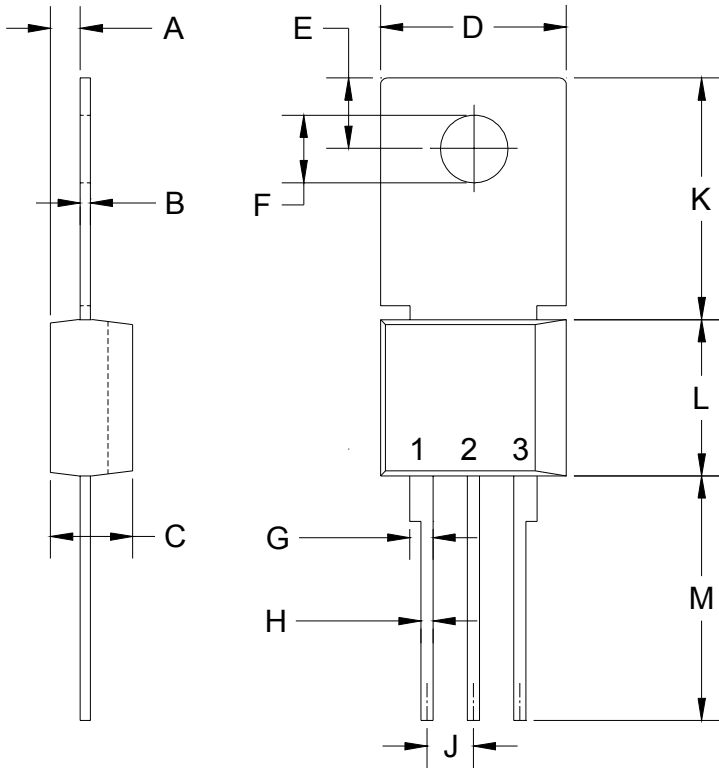
	SYMBOL	C106A1	C106B1	C106C1	C106D1	C106E1	C106M1	UNITS
Peak Repetitive Off-State Voltage	V _{DRM} , V _{RRM}	100	200	300	400	500	600	V
RMS On-State Current	I _{T(RMS)}				4.0			A
Peak One Cycle Surge (60Hz)	I _{TSM}				20			A
I ² t Value for Fusing (t>1.5ms)	I ² t				0.5			A ² s
Peak Gate Power	P _{GM}				0.5			W
Average Gate Power	P _{G(AV)}				0.1			W
Peak Forward Gate Current	I _{GFM}				0.2			A
Peak Reverse Gate Voltage	V _{GRM}				6.0			V
Storage Temperature	T _{stg}			-40 to +150				°C
Junction Temperature	T _J			-40 to +110				°C
Thermal Resistance (Case)	θ _{J-C}			3.0				°C/W
Thermal Resistance	θ _{J-A}			75				°C/W

ELECTRICAL CHARACTERISTICS (T_C=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{DRM} , I _{RRM}	Rated V _{DRM} , V _{RRM} , R _{GK} =1kΩ			10	μA
I _{DRM} , I _{RRM}	Rated V _{DRM} , V _{RRM} , R _{GK} =1kΩ, T _C =110°C			100	μA
V _{TM}	I _{FM} =4.0A			2.2	V
I _{GT}	V _{AK} =6.0V, R _L =100Ω, R _{GK} =1kΩ			200	μA
I _{GT}	V _{AK} =6.0V, R _L =100Ω, R _{GK} =1kΩ, T _C =-40°C			500	μA
V _{GT}	V _{AK} =6.0V, R _L =100Ω, R _{GK} =1kΩ	0.4		0.8	V
V _{GT}	V _{AK} =6.0V, R _L =100Ω, R _{GK} =1kΩ, T _C =-40°C	0.5		1.0	V
V _{GT}	V _{AK} =Rated V _{DRM} , R _L =3kΩ, R _{GK} =1kΩ, T _C =110°C	0.2			V
I _{HX}	V _D =12V, R _{GK} =1kΩ	0.3		3.0	mA
I _{HX}	V _D =12V, R _{GK} =1kΩ, T _C =-40°C	0.4		6.0	mA
I _{HX}	V _D =12V, R _{GK} =1kΩ, T _C =110°C	0.14		2.0	mA
I _{LX}	V _D =12V, R _{GK} =1kΩ	0.3		4.0	mA
I _{LX}	V _D =12V, R _{GK} =1kΩ, T _C =-40°C	0.4		8.0	mA
dv/dt	V _D =Rated V _{DRM} , R _{GK} =1kΩ, T _C =110°C		8.0		V/μs
t _{gt} (turn-on time)			1.2		μs
t _q (turn-off time)			40		μs

(SEE REVERSE SIDE)

TO-202 PACKAGE - MECHANICAL OUTLINE



SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.055	0.071	1.40	1.80
B	0.016	0.024	0.40	0.60
C	0.173	0.181	4.40	4.60
D	0.374	0.413	9.50	10.50
E	0.146	0.154	3.70	3.90
F (DIA)	0.142	0.150	3.60	3.80
G	0.039	0.055	1.00	1.40
H	0.024	0.031	0.60	0.80
J	0.094	0.106	2.39	2.69
K	0.492	0.551	12.50	14.00
L	0.327	0.346	8.30	8.80
M	0.492	0.531	12.50	13.50

TO-202 (REV: R1)

R1

Lead Code:

- 1) Cathode
- 2) Anode
- 3) Gate

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