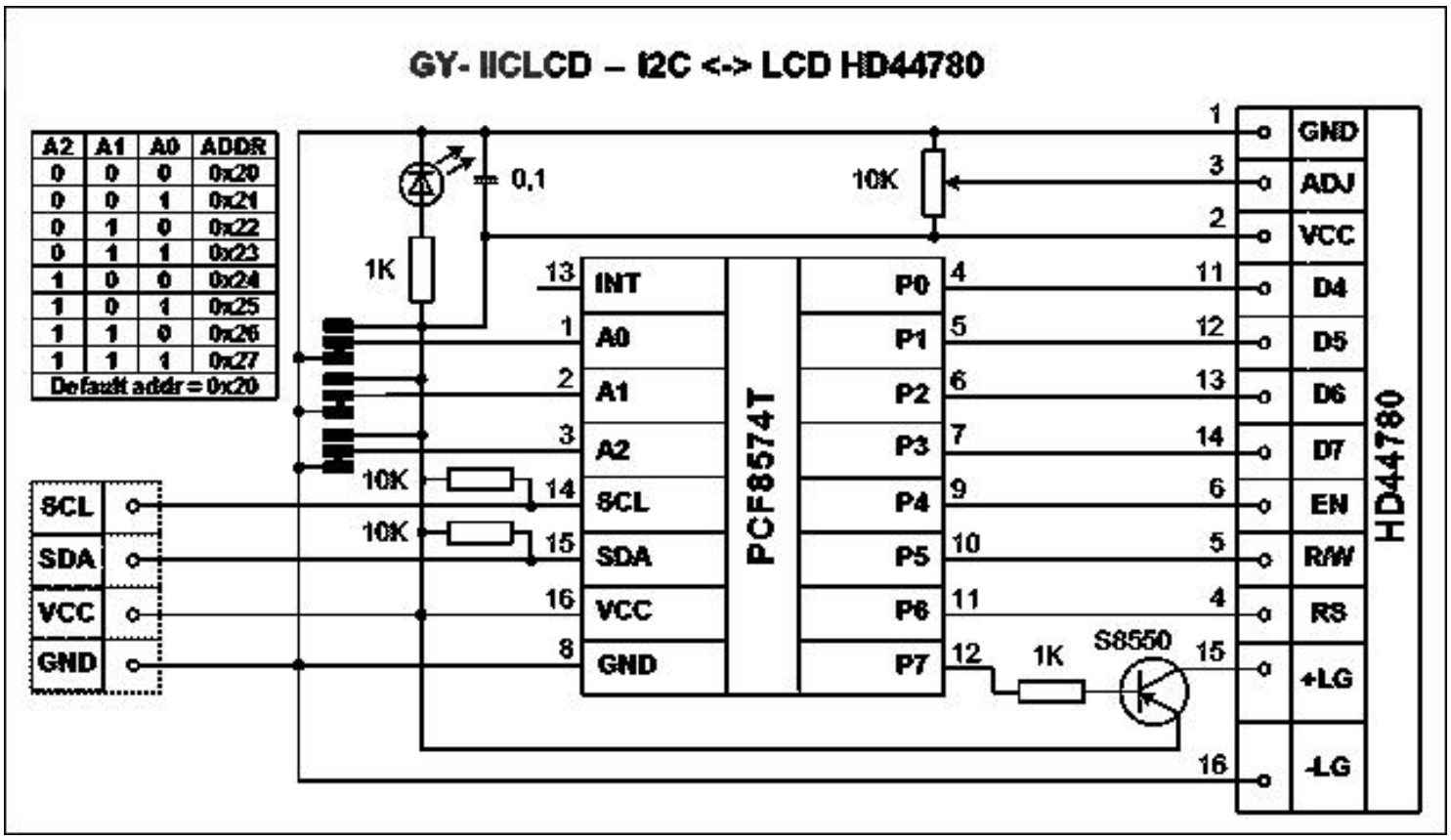
GY-IICLCD


GY- IICLCD – I2C <-> LCD HD44780
A2 A1 A0 ADDR
0 0 0 0x20
0 0 1 0x21
0 1 0 0x22
0 1 1 0x23
1 0 0 0x24
1 0 1 0x25
1 1 0 0x26
1 1 1 0x27
Default addr = 0x20
0,1
1K
10K
10K
10K
SCL
SDA
VCC
GND
INT
A0
A1
A2
SCL
SDA
VCC
GND
PCF8574T
P0
P1
P2
P3
P4
P5
P6
P7
S8550
1K
GND
ADJ
VCC
D4
D5
D6
D7
EN
R/W
RS
+LG
-LG
HD44780