

# KA4558

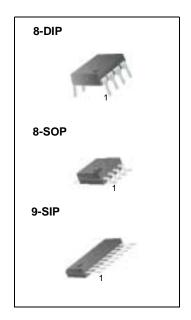
# **Dual Operational Amplifier**

### **Features**

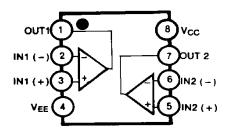
- No frequency compensation required.
- No latch up.
- Large common mode and differential voltage range.
- Parameter tracking over temperature range.
- Gain and phase match between amplifiers.
- Internally frequency compensated.
- Low noise input transistors.

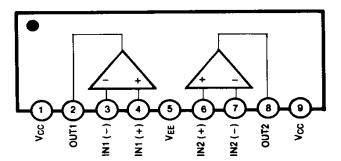
### **Descriptions**

The KA4558 is a monolithic integrated circuit designed for dual operational amplifier.



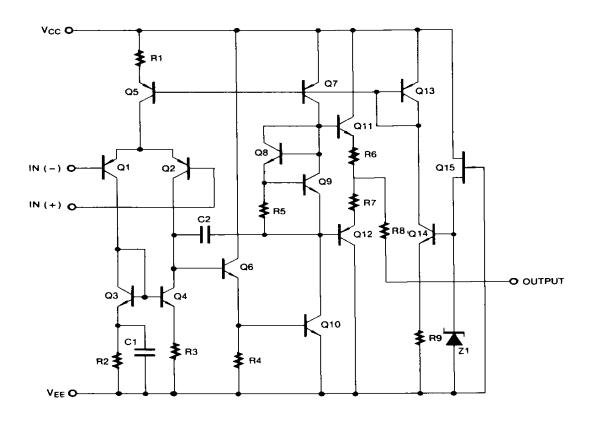
## **Internal Block Diagram**





## **Schematic Diagram**

(One Section Only)



# **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	±22	V
Differential Input Voltage	VI(DIFF)	30	V
Input Voltage	VI	±15	V
Power Dissipation	PD	400	mW
Operating Temperature Range KA4558 KA4558I	Topr	0 ~ 70 -40 ~ 85	°C
Storage Temperature Range	TSTG	-65 ~ 150	°C

### **Electrical Characteristics**

(VCC = 15V, VEE = - 15V, TA = 25  $^{\circ}$ C unless otherwise specified)

_ ,		2 111		KA4558/KA4558I			
Parameter Symbol Conditions		ditions	Min	Тур	Max	Unit	
Input Offset Voltage	1/10	Rs≤10KΩ		-	2	6	m\/
	VIO		Note 1	-	-	7.5	- mV
				-	5	200	
Input Offset Current	lio		T <sub>A</sub> =T <sub>A</sub> (MAX)	-	-	300	nA
			TA =TA(MIN)	-	-	300	
		T <sub>A</sub> =T <sub>A</sub> (MAX)		-	30	500	nA
Input Bias Current	IBIAS			-	-	800	
			TA =TA(MIN)	-	-	- 800	
Large Signal	Gv	$V_{O(P-P)} = \pm 10V, R_{L} \le 2K\Omega$		20	200	-	V/mV
Voltage Gain	G <sub>V</sub>		Note 1	-	-	-	V/IIIV
Common Mode Input Voltage Range	Vivn			±12		-	V
	V <sub>I(R)</sub>		Note 1	-	-	-	1
Common Mode Rejection Ratio	CMRR	Rs≤10KΩ		70	90	-	- dB
	CIVILLIX		Note 1	-	-	-	
Supply Voltage Rejection Ratio	PSRR	Rs≤10KΩ	s≤10KΩ		90	-	dB
	FORK		Note 1	76	90	-	ub
Output Voltage Swing	VO(P-P)	RL≥10KΩ	- Note1	±12	±14	-	V
	VO(P-P)	RL≥2KΩ	Note	±10	±13	-	
				-	3.5	5.8	
Supply Current (Both Amplifiers)	Icc		$T_A = T_A(MAX)$	-	-	5.0	mA
(Both Ampliners)			TA =TA(MIN)	-	-	6.7	]
Power Consumption (Both Amplifiers)			_	-	70	170	
	PC	TA = TA(MAX)		-	-	150	mW
			$T_a = T_A(MIN)$		-	200	
Slew Rate (Note2)	SR	VI =10V, RL≥2KΩ CI≤100pF		1.2	-	-	V/μs
Rise Time (Note2)	TR	V <sub>I</sub> =20mV, R <sub>L</sub> ≥2KΩ C <sub>I</sub> ≤100pF		-	0.3	-	μs
Overshoot (Note2)	os	V <sub>I</sub> =20mV, R <sub>L</sub> ≥2KΩ C <sub>I</sub> ≤100pF		-	15	-	%

### Note:

 $<sup>1. \;</sup> KA4558 : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = 0 \leq T_{A} \leq 70 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \; ^{\circ}C \; , \; KA4558I : T_{A(MIN)} \leq T_{A} \leq T_{A(MIN)} \leq T_{A} \leq T_{$ 

<sup>2.</sup> Guaranteed by design.

## **Typical Performance Characteristics**

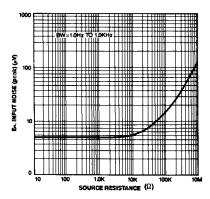


Figure 1. Burst Noise vs Source Resistance

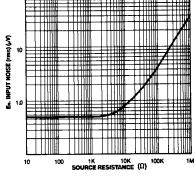


Figure 2. RMS Noise vs Source Resistance

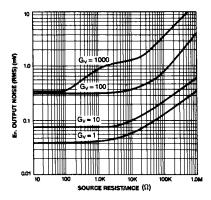


Figure 3. Output Noise vs Source Resistance

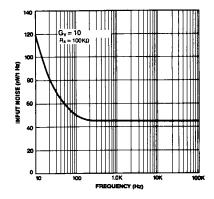


Figure 4. Spectral Noise Density

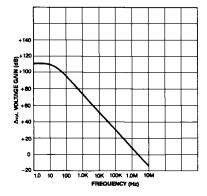


Figure 5. Open Loop Frequency Response

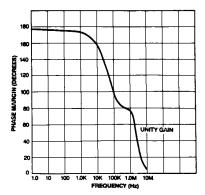


Figure 6. Phase Margin vs Frequency

## **Typical Performance Characteristics (continued)**

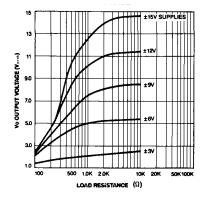


Figure 7. Positive Output Voltage Swing vs Load Resistance

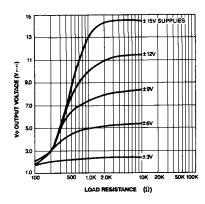


Figure 8. Negative Output Voltage Swing vs Load Resistance

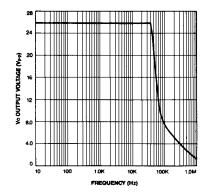
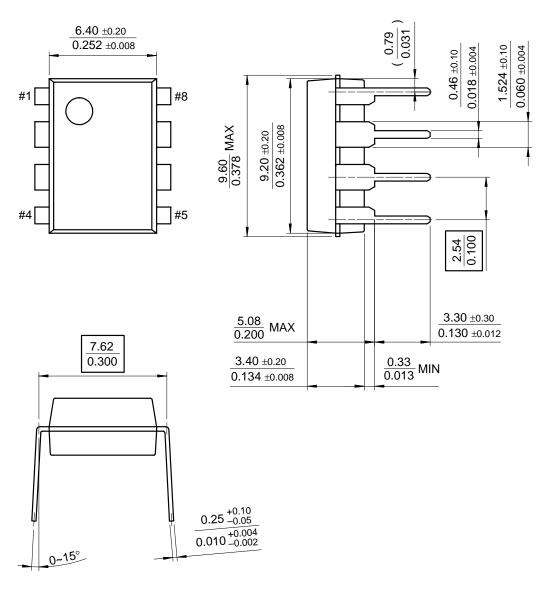


Figure 9. Power Bandwidth (Large Signal Output Swing vs Frequency)

### **Mechanical Dimensions**

### **Package**

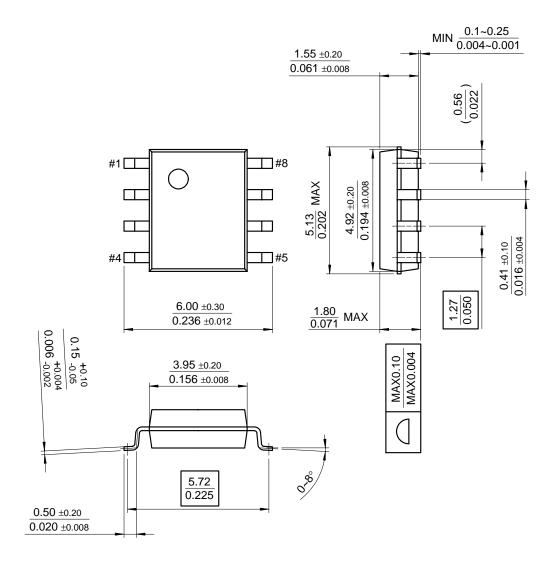
# 8-DIP



## **Mechanical Dimensions** (Continued)

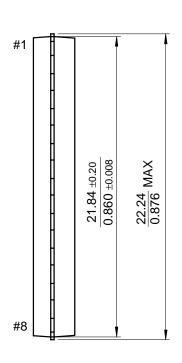
### **Package**

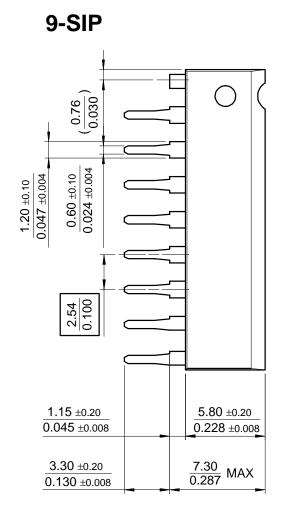
# 8-SOP

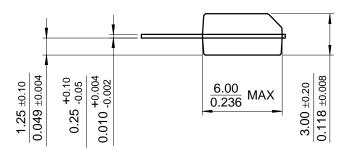


### **Mechanical Dimensions** (Continued)

### Package







# **Ordering Information**

Product Number	Package	Operating Temperature
KA4558	8-DIP	
KA4558D	8-SOP	0 ~ + 70°C
KA4558S	9-SIP	
KA4558I	8-DIP	-40 ~ + 85°C

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