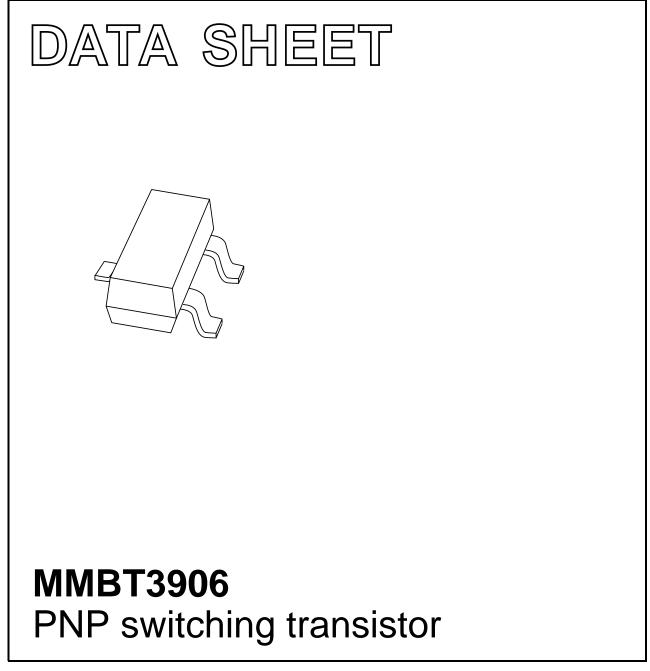
DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 2000 Apr 11 2003 Mar 18



MMBT3906

FEATURES

- Collector current capability $I_C = -200 \text{ mA}$
- Collector-emitter voltage $V_{CEO} = -40$ V.

APPLICATIONS

• General switching and amplification.

DESCRIPTION

PNP switching transistor in a SOT23 plastic package. NPN complement: MMBT3904.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾	
MMBT3906	7B*	

Note

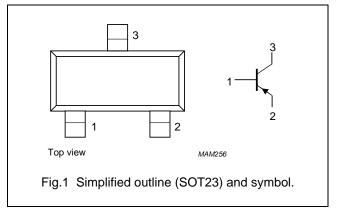
- 1. * = p: Made in Hong Kong.
 - * = t: Made in Malaysia.
 - * = W: Made in China.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	UNIT
V _{CEO}	collector-emitter voltage -40 V		V
I _C	collector current (DC)	-200	mA

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	-40	V
V _{CEO}	collector-emitter voltage	open base	-	-40	V
V _{EBO}	emitter-base voltage	open collector	-	-6	V
I _C	collector current (DC)		-	-200	mA
I _{CM}	peak collector current		-	-200	mA
I _{BM}	peak base current		-	-100	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$; note 1	-	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

Note

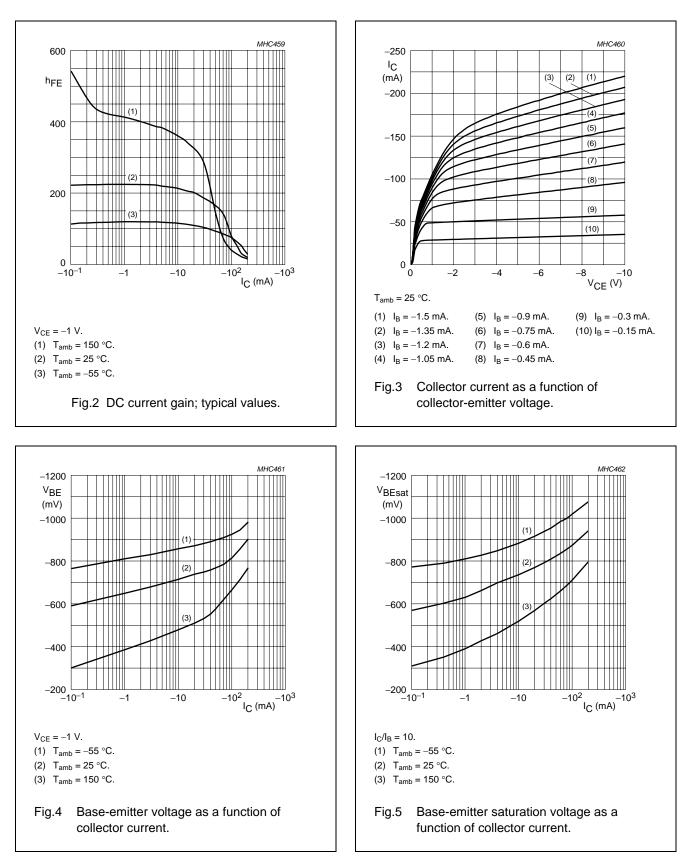
1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

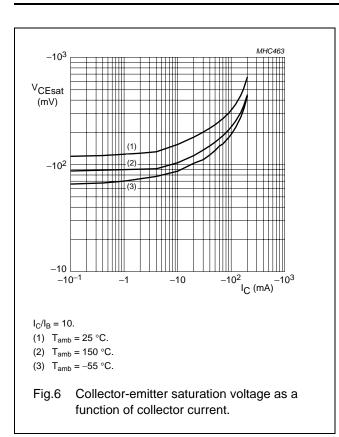
 T_{amb} = 25 °C unless otherwise specified.

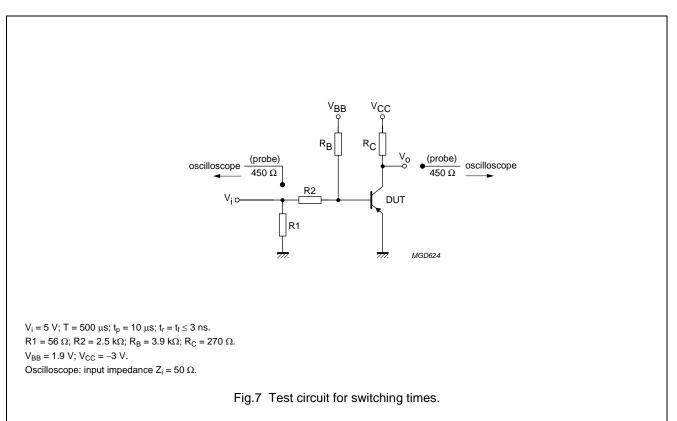
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	$I_E = 0; V_{CB} = -30 \text{ V}$	-	-50	nA
I _{EBO}	emitter cut-off current	$I_{C} = 0; V_{EB} = -6 V$	-	-50	nA
h _{FE}	DC current gain	$V_{CE} = -1$ V; see Fig.2			
		$I_{\rm C} = -0.1 \rm{mA}$	60	-	
		$I_{\rm C} = -1 \rm{mA}$	80	-	
		I _C = -10 mA	100	300	
		I _C = -50 mA	60	-	
		I _C = -100 mA	30	-	
V _{CEsat}	collector-emitter saturation voltage	$I_{\rm C} = -10 \text{ mA}; I_{\rm B} = -1 \text{ mA}$	-	-250	mV
		$I_{\rm C} = -50 \text{ mA}; I_{\rm B} = -5 \text{ mA}$	-	-400	mV
V _{BEsat}	base-emitter saturation voltage	$I_{\rm C} = -10 \text{ mA}; I_{\rm B} = -1 \text{ mA}$	-	-850	mV
		$I_{\rm C} = -50 \text{ mA}; I_{\rm B} = -5 \text{ mA}$	_	-950	mV
C _c	collector capacitance	$I_E = i_e = 0; V_{CB} = -5 V; f = 1 MHz$	-	4.5	pF
C _e	emitter capacitance	$I_{C} = i_{c} = 0; V_{EB} = -500 \text{ mV};$ f = 1 MHz	-	10	pF
f _T	transition frequency	$I_{C} = -10 \text{ mA}; V_{CE} = -20 \text{ V};$ f = 100 MHz	250	-	MHz
F	noise figure	$I_{C} = -100 \ \mu A; V_{CE} = -5 \ V;$ R _S = 1 kΩ; f = 10 Hz to 15.7 kHz	_	4	dB
Switching ti	mes (between 10% and 90% lev	els); see Fig.7			
t _d	delay time	$I_{Con} = -10 \text{ mA}; I_{Bon} = -1 \text{ mA};$	_	35	ns
t _r	rise time	I _{Boff} = 1 mA	_	35	ns
t _s	storage time	1	-	225	ns
t _f	fall time	1	_	75	ns

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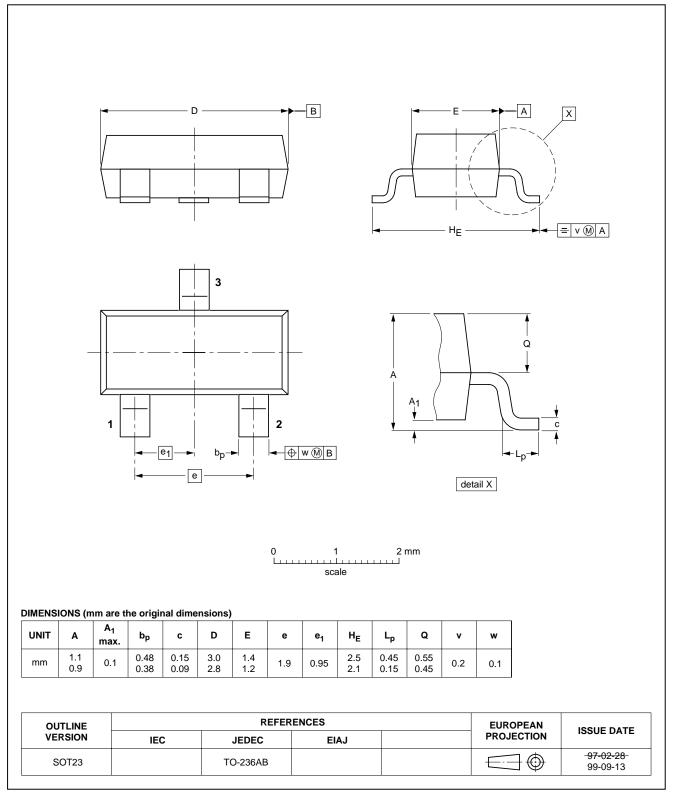
MMBT3906





PACKAGE OUTLINE





MMBT3906

SOT23

MMBT3906

DATA SHEET STATU	S
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DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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