



ELECTRONICS, INC.
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NTE1818 Integrated Circuit Module, AF PO, 25W/Ch, Dual Power Supply

Features:

- Muting circuit to cut off pop noise
- Greatly reduced heat sink due to case temperature +125°C guaranteed

Absolute Maximum Ratings: ($T_A = +25^\circ\text{C}$ unless otherwise specified)

Maximum Supply Voltage, $V_{CC\ max}$	±39V
Thermal Resistance, Junction-to-Case, R_{thJC}	2.6°C/W
Junction Temperature, T_J	150°C
Operating Case Temperature, T_C	125°C
Storage Temperature Range, T_{stg}	-30° to +125°C
Available Time for Load Shorted ($V_{CC} = \pm 26V, R_L = 8\Omega, f = 50Hz, P_O = 25W$), t_s	2sec

Recommended Operating Conditions: ($T_A = +25^\circ\text{C}$ unless otherwise specified)

Recommended Operating Voltage, V_{CC}	±26V
Load Resistance, R_L	8Ω

Operating Characteristics: ($T_A = +25^\circ\text{C}, V_{CC} = \pm 26V, R_L = 8\Omega, R_g = 600\Omega, VG = 40dB$,
 R_L : non-inductive load, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Quiescent Current	I_{CCO}	$V_{CC} = \pm 31V$	20	40	100	mA
Output Power	$P_{O(1)}$	THD – 0.4%, $f = 20Hz \sim 20kHz$	25	–	–	W
	$P_{O(2)}$	$V_{CC} = \pm 22V, THD = 1.0\%$	25	–	–	W
Total Harmonic Distortion	THD	$P_O = 1.0W, f = 1kHz$	–	–	0.3	%
Frequency Response	f_L, f_H	$P_O = 1.0W, -3dB$	20 to 50k			Hz
Input Resistance	r_i	$P_O = 1.0W, f = 1kHz$	–	55	–	kΩ
Output Noise Voltage	V_{NO}	$V_{CC} = \pm 31V, R_g = 10k\Omega$	–	–	1.2	mV _{rms}
Midpoint Voltage	V_N	$V_{CC} = \pm 31V$	-70	0	70	mV
Muting Voltage	V_M		-2	-5	-10	V

Pin Connection Diagram

18	Rt Ch Input (-)
17	Rt Ch Input (+)
16	GND
15	Compensation
14	(-) V _{CC}
13	Rt Ch Output
12	Bypass
11	(+) V _{CC}
10	Lt Ch Output
9	(-) V _{CC}
8	Compensation
7	Compensation
6	Muting
5	Compensation
4	Compensation
3	Compensation
2	Lt Ch Input (+)
1	Lt Ch Input (-)

